

**KENDRIYA VIDYALAYA DUNGARPUR**  
**PROJECT ASSIGNMENT DETAIL FOR CLASS XII 2018-19**

SNO	SUBJECT	NAME OF STUDENTS	TOPIC	DATE OF ASSIGNMENT	REMARK
1	IP	MIHIR PANCHAL	HOTEL RESERVATION SYSTEM	08/05/2018	NetBean & MYSQL
		KARTIK PATIDAR			
		ISHAN JAIN	VISUAL MOBILE		
		ANOOP CHICK			
		JAINISH JAIN	LIBRARY MANAGEMENT		
		KUSH JAIN			
		VAIBHAV PANCHAL	FASHION STORE		
		PALLAV JOSHI			
2	CHEMISTRY	NISHI RAWAL	Content of Cold drink.	08/05/2018	
		VAIBHAV PANCHAL			
		NIKEETA AHART	Content of tooth powder		
		KARTIK PATIDAR	pH of Fruits & Vegetables		
		ISHA JAIN	Preparation of Rayon		
		PALLAV JOSHI	threads from cellulose.		
		MIHIR PANCHAL	Study of quantity of		
		ANOOP CHICK	casein present in different		
			samples of milk.		
	CHEMISTRY	ISHAN JAIN	Study of common food	08/05/2018	
		MAYANK ROAT	adulterants in various		
			food material.		
		KARMVEER SINGH	Test of Carbohydrates,		
		JIGAR YADAV	protein & fats in various		
			food material		
		KUSH JAIN	The Highest pH level in		
			different samples of Shampoo		
		DEEPESH KHARADI	Determination of Lead		
			in lipstick		
	CHEMISTRY	KUNAI PARMAR	Preparation of soya bean		
		BHAVYA MALI	milk and its comparison	08/05/2018	
			with natural milk with		
			respect to curd formation,		
			effect of temperature etc.		
		MATANGI PANDYA	Analysis of chocolate		
		NIKITA KATARA	Soap formation from guava		
			leaves extract		



Investigatory Project / XII SC / PHYSICS, 08.05.2018

SNO	SUBJECT	NAME OF STUDENTS	TOPIC	DATE OF ASSIGNMENT	Sign REMARK
1		Anoop Chik	To design an appropriate logic gate combination for a given Truth Table		<u>Anoop</u>
2	PHYSICS	Bhanya Mali	To study Various factors on which the internal resist./EMF of a Cell depend.		<u>Bhanya</u>
3		Deepesh Kharadi	To estimate charge induced on each Pith balls suspended by Coulomb Law		<u>Deepesh</u>
4		Isha Jain	To design an appropriate logic gate combination for a given Truth Table		<u>Isha</u>
5		Ishan Jain	To design an appropriate logic gate combination for a given Truth Table		<u>Ishan</u>
6	PHYSICS	Jigar Yadav	To study Various factor on which the internal resistance/EMF of a Cell depend.		<u>Jigar</u>
7		Karmveer Singh	To study Various factors on which the internal resistance/EMF of a Cell depend		<u>Karm</u>
8		Kartik Patidar	To design an appropriate logic gate combination for given Truth		<u>Kartik</u>
9		Kunal Parmar	TO DESIGN AN APPROPRIET LOGIC GATE COMBINATION FOR GIVEN TRUTH TABLE		<u>Kunal</u>
10		Kush Jain	To set up a common transistor circuit and to study its input & output characteristics		<u>Kush</u>
11		Matangi Pandya	To set up a common transistor circuit and to study its input & output.		<u>Matangi</u>
12		Mayank Roat	To study various factors on which the internal resistance/EMF of a cell depends		<u>May</u>
13	PHYSICS	Mihir Panchal	To study various factors on which the internal resistance/EMF of a cell depends.		<u>Mihir</u>
14		Nikeeta Ahari	To study various factor on which EMF of a cell depends.		<u>Nikeeta</u>
15		Niketa Katara	To study various factor on which the internal resistance/EMF of a cell depends		<u>Niketa</u>
16		Nishi Rawal	To design an appropriate logic gate combination for given table		<u>Nishi</u>
17	PHYSICS	Pallavi Joshi	To study factor on which self inductance of coil depends by observing effect of this coil when put in series with a resistor in a circuit fed up by an A.C source adjustable frequency.		<u>Pallavi</u>
18		Vaibhav Panchal.	Observing effect of this coil when put in series with a resistor in a circuit fed up by an A.C source adjustable frequency.		<u>Vaibhav</u>

(S.P. Bairwa)  
PHYSICS



